

FIG.1

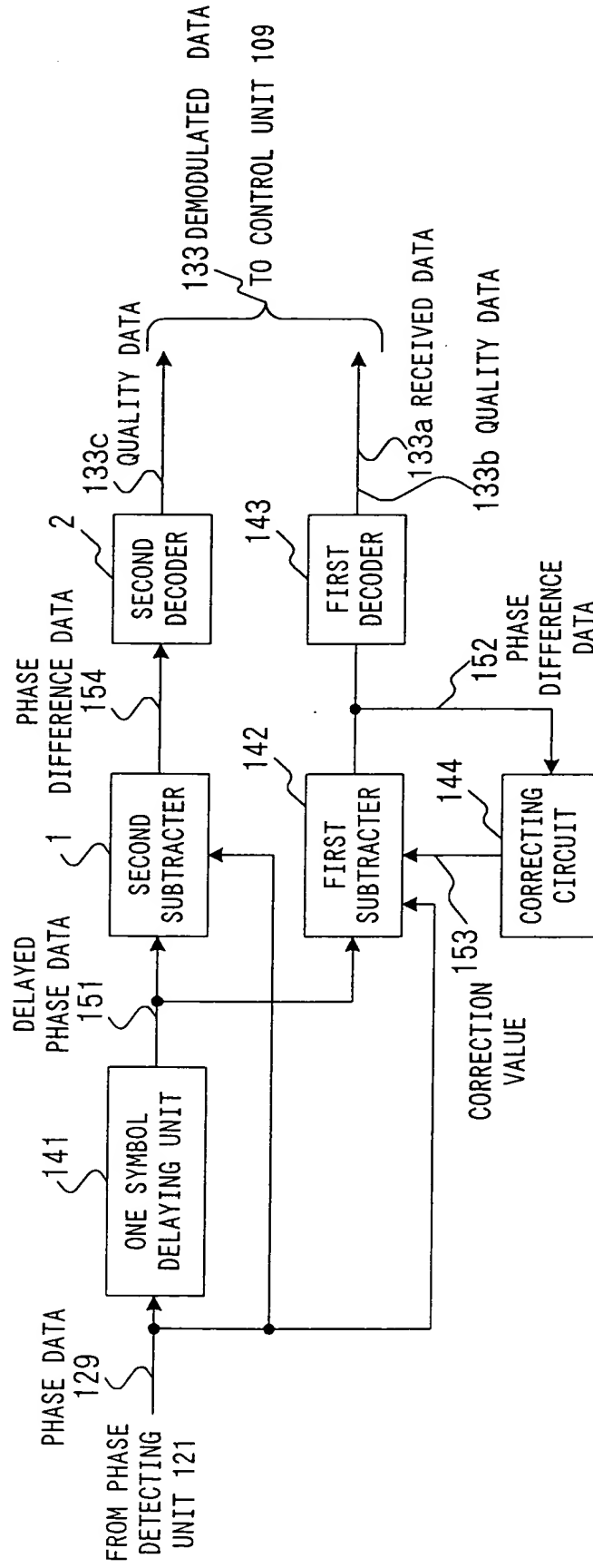


FIG.2

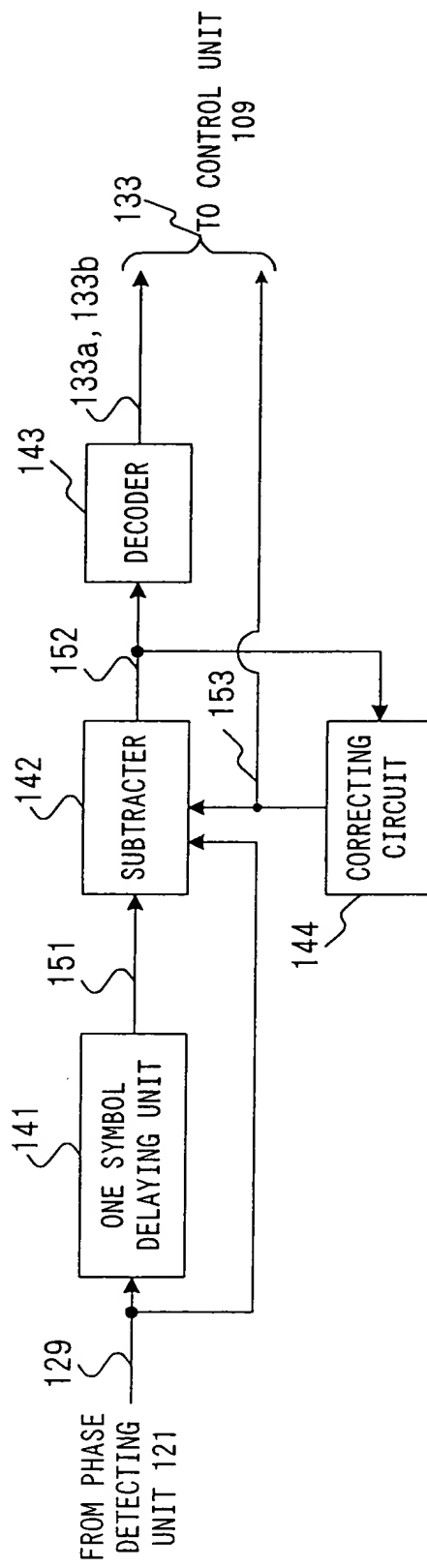


FIG.3

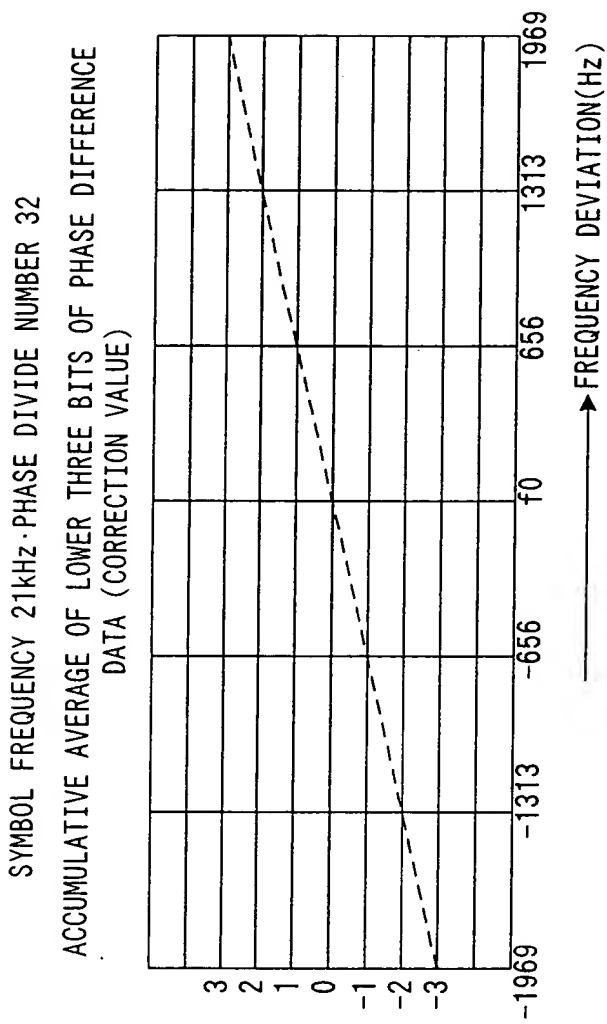


FIG.4

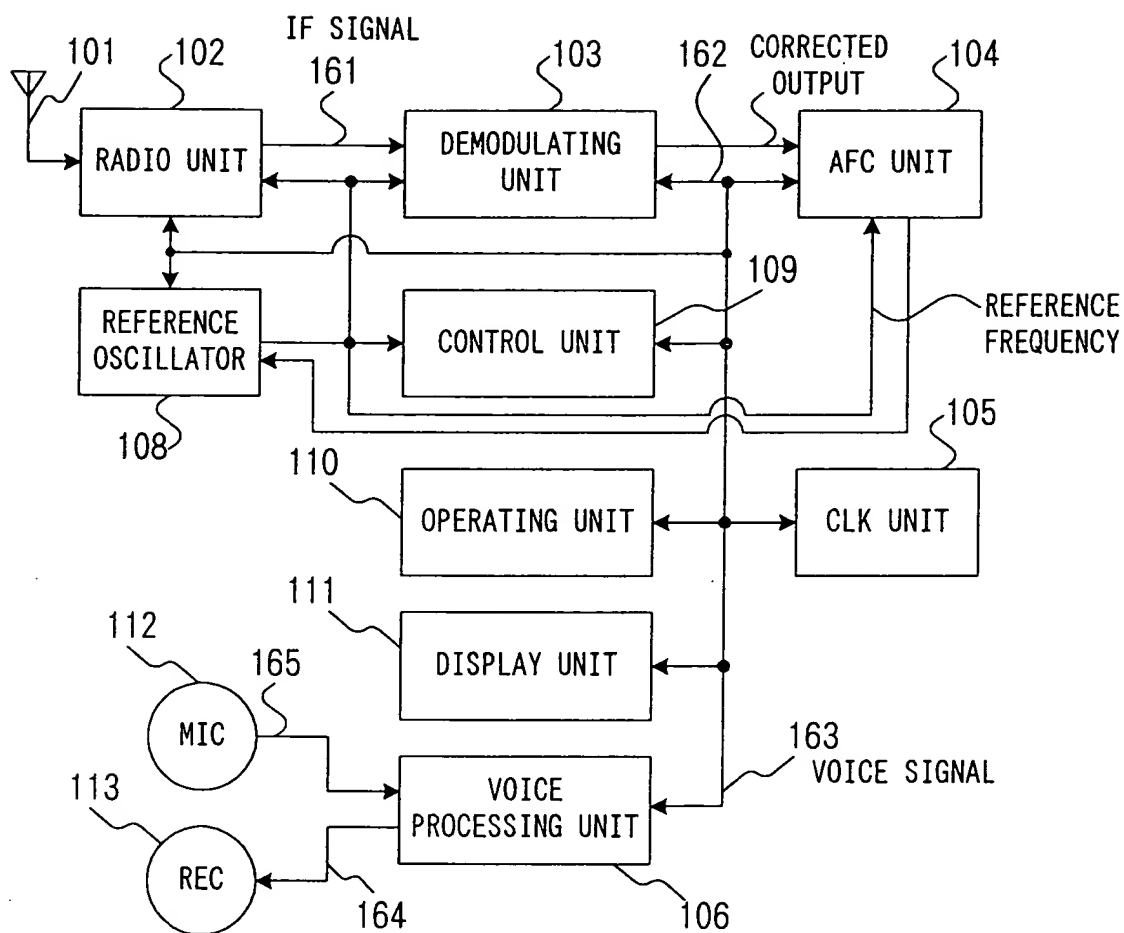


FIG. 5

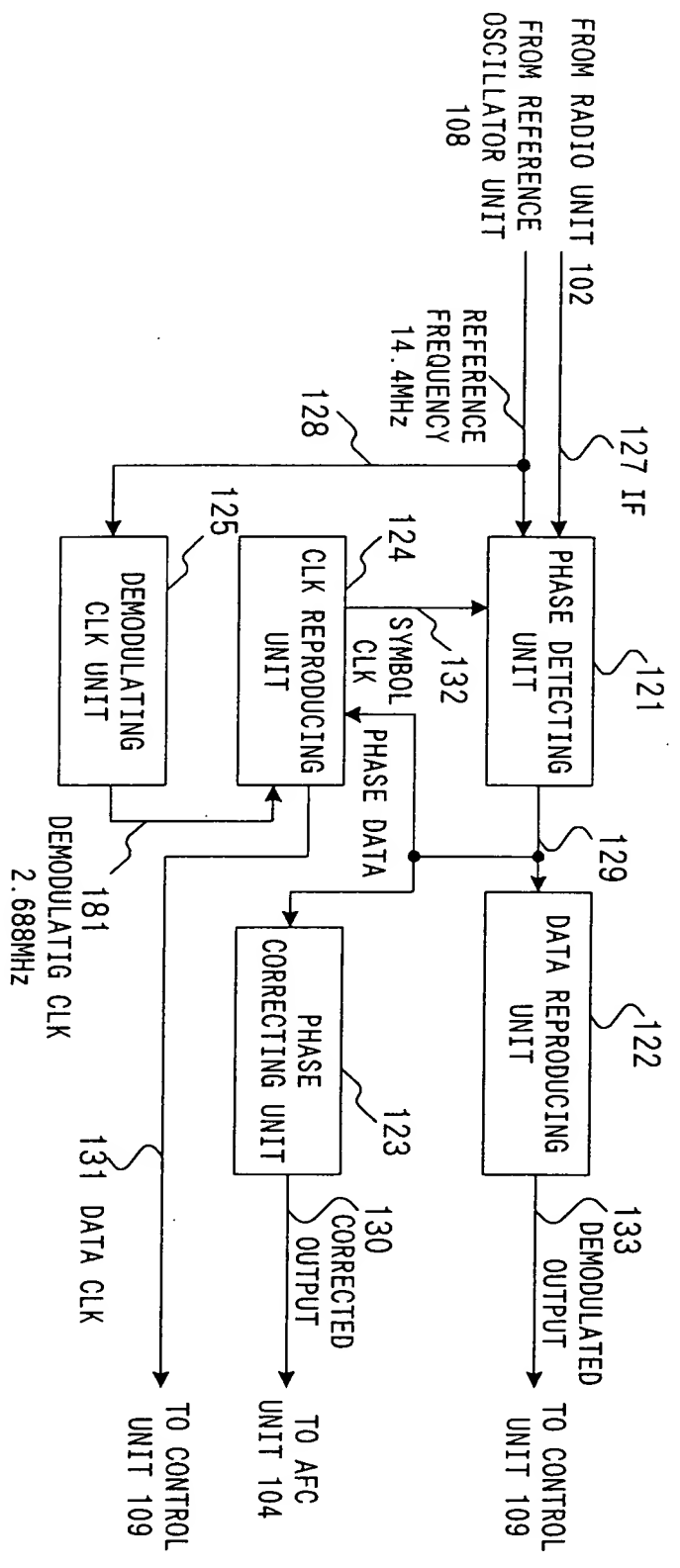


FIG. 5 is a block diagram of a demodulation system according to the present invention.

FIG. 6

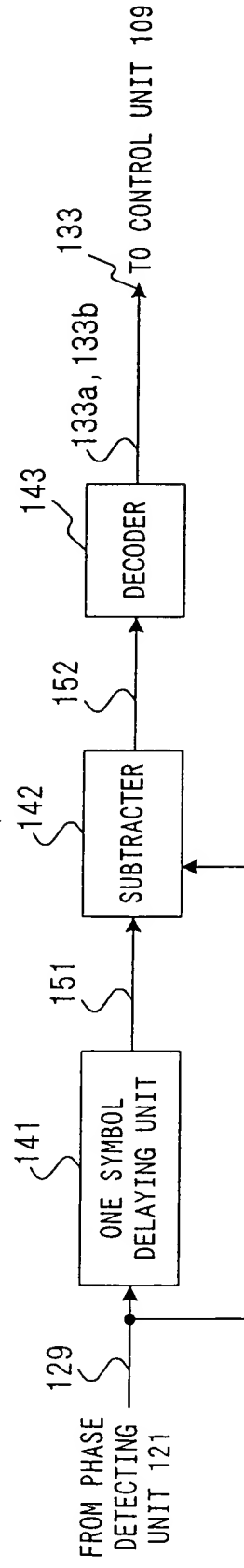


FIG. 7

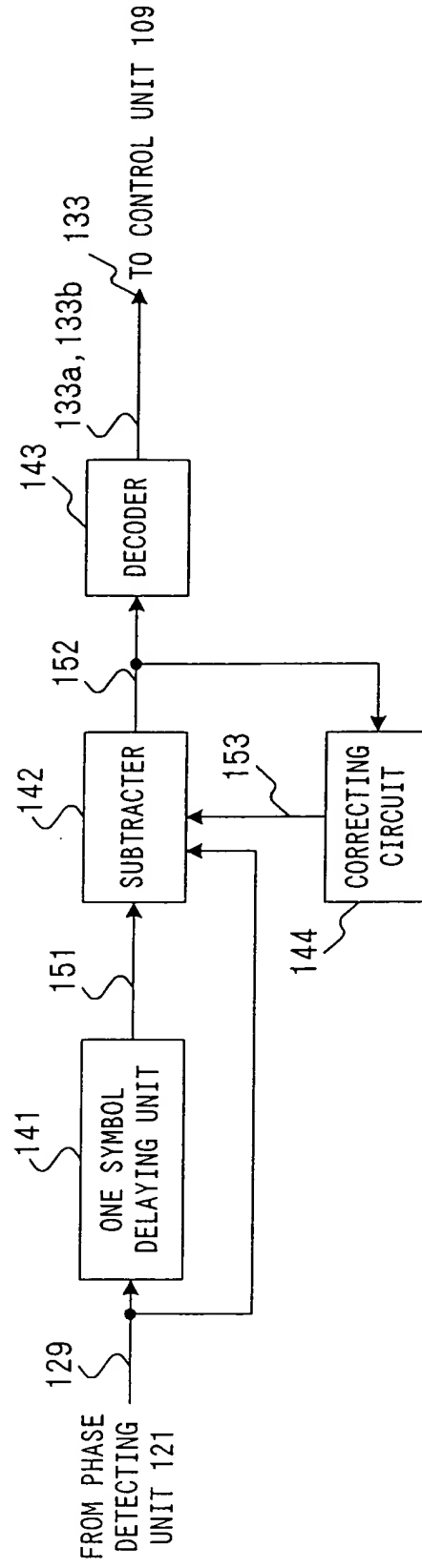


FIG. 8

